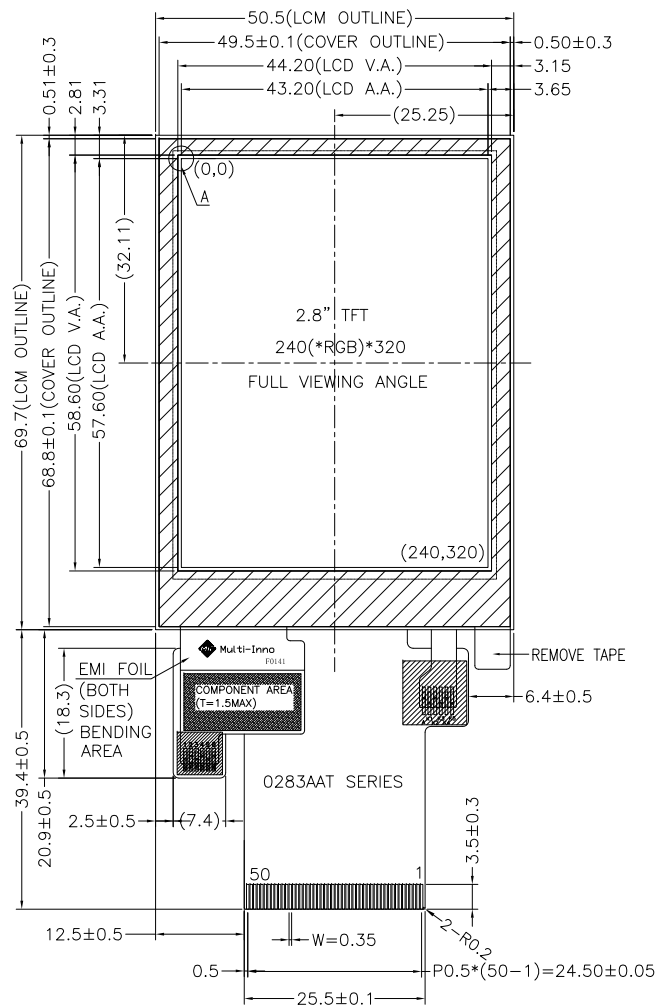


NO MECHANICAL HOUSING TO HOLD THE TFT AND THE TOUCH PANEL ON THIS PRODUCT, SO THE END UNIT'S MECHANICAL DESIGN MUST INCLUDE A HOUSING FOR PREVENTING THE SEPARATION OF THE TFT AND THE TOUCH PANEL



NOTES FOR TFT:

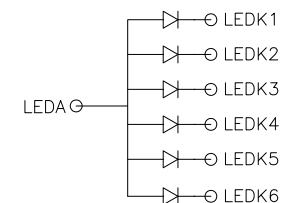
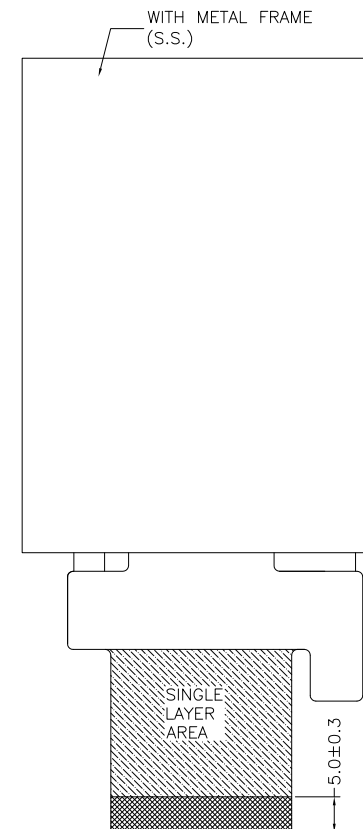
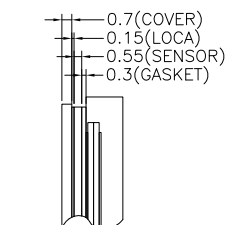
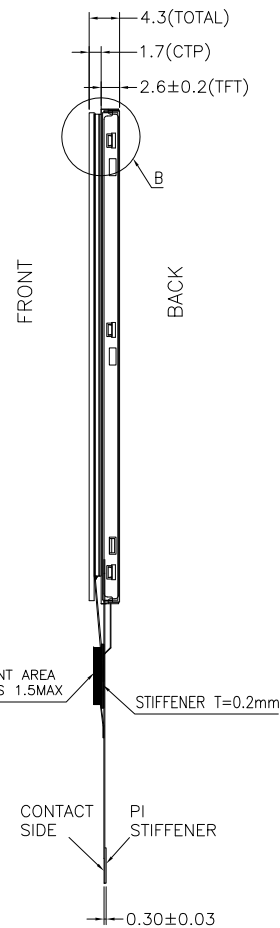
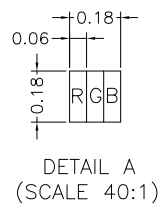
1. DISPLAY TYPE: IPS TFT/TRANSMISSIVE/NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=2.8V(TYP.), VDDI=1.8V/2.8V(TYP.)
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. DRIVER IC: ST7789V2
5. BACKLIGHT: 6-CHIP LED, IF=120mA, VF=2.8V~3.4V
6. SURFACE LUMINANCE: 425CD/M²(TYP.)
7. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE

OTHERS:

1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. GENERAL TOLERANCE: ±0.2mm
4. ROHS COMPLIANT

NOTES FOR CTP:

1. 2.83" PROJECTIVE CAPACITIVE TOUCH PANEL
2. OPERATING VOLTAGE: VDD=2.8V
3. COVER GLASS + SENSOR GLASS + FPCA
4. DRIVER IC: FT5446-P03(SMT), IIC INTERFACE
5. MULTI-FINGER: UP TO 10
6. SURFACE HARDNESS OF C/L: >=7H (750g)
7. FIRMWARE FILE: MI0283CCP-C22(F5446-P03)_FIRMWARE_V09_20200519



BACKLIGHT CIRCUIT DIAGRAM

PIN DESCRIPTION	
PIN	SYMBOL
1	GND
2	SCL
3	SDA
4	RST
5	INT
6	VDDI
7	VDD
8	VDD
9	IM3
10	IM2
11	IM1
12	IMO
13	RESET
14	CS
15	SDA
16	RS(SPI-SCL)
17	WR(SPI-RS)
18	RD
19	VSYNC
20	HSYNC
21	ENABLE
22	DOTCLK
23	DB00
24	DB01
25	DB02
26	DB03
27	DB04
28	DB05
29	DB06
30	DB07
31	DB08
32	DB09
33	DB10
34	DB11
35	DB12
36	DB13
37	DB14
38	DB15
39	DB16
40	DB17
41	SDO
42	GND
43	LEDA
44	LEDK1
45	LEDK2
46	LEDK3
47	LEDK4
48	LEDK5
49	LEDK6
50	GND



MULTI-INNO TECHNOLOGY CO., LTD.

04	UPDATE THE IC P/N AND FIRMWARE VERSION NUMBER OF THE CTP	RITA	2019.08.14	DRAWN BY:	RITA	2019.08.14	SCALE : 1/1	MODULE P/N:
03	CORRECT OPERATING VOLTAGE FOR CTP FROM 3.3V TO 2.8V	CAROL	2021.06.23	CHECKED BY:			UNIT: mm	MI0283AAT-7CP2
02	CORRECT DRIVER IC FROM ST7789V TO ST7789V2	CAROL	2021.02.24	APPROVED BY:			SHEET NO.: 1/1	DESCRIPTION:
01	FIRST ISSUE	RITA	2019.08.14	VERSION NO:	04			TFT + CTP MODULE
VER.	REVISED DESCRIPTION	REVISER	DATE					